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			Application / Conf. No.	09/724,652 / 7708
TRANSMITTAL			Filing Date	November 28, 2000
FORM			First Named Inventor	Raymond C. Pang
(to be used for all correspondence after initial filing)			Examiner Name	Not Yet Known
Mail Stop:			Art Unit	2131
Express Mail Receipt No.			Patent No.	
Total Number of Pages in This Submission		37	Attorney Docket Number	r X-805-1 US
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Parts under 37 CFR 1.52 or 1.53				
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IN THE UNITED STATES PATENT OFFICE

plicants:

Raymond C. Pang et al.

Assignee:

Xilinx, Inc.

Title:

"Programmable Logic Device with Decryption

Algorithm and Decryption Key"

Serial No.:

09/724,652

File Date:

November 28, 2000

Examiner:

Unknown

Art Unit:

2131

Docket No.:

X-805-1 US

Conf. No.:

7708

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the four (4) references listed in the attached Form PTO-A copy of each is enclosed herein.

These references were cited in a related foreign application. This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Attorney for Applicants

Reg. No. 51,959

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